Refine Search

Search Results -

Terms	Documents
L9	0

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: 10/708,341

Refine Search

Recall Text | Clear | Interrupt

Search History

DATE: Friday, June 22, 2007 Purge Queries Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=OR		
L13	L9	0	L13
L12	l3 and L9	0	L12
	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR		
L11	L9 and @ad<=20030227	3	L11
L10	l3 and L9	0	L10
L9	bicycl$ and remov$ and guderzo$	13	L9
L8	bicycl$ and remov$ and guderso$	0	L8
L7	bicycl$. and remov$ and guderso$	0	L7
L6	l3 and L5	4	L6
L5	bicycl$.clm. and remov$.clm. and @ad<=20030227	1708	L5
	DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=OR		
L4	L3 and bicycle.ab. (display$ same (accumulat$ with (information or data or value))) and	3	L4

L3	@ad<=20030227	6354	L3
L2	L1 and (sum$ or total$ or accumulat$ or add$)	2	L2
L1	5261858.pn. or 6490507.pn.	2	L1

END OF SEARCH HISTORY

Hit List

First Hit

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The probable cause is use of unlimited truncation. Revise your search strategy to use limited truncation.

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 1 through 10 of 13 returned.

☐ 1. Document ID: US 20050043129 A1

L9: Entry 1 of 13 File: PGPB Feb 24, 2005

PGPUB-DOCUMENT-NUMBER: 20050043129
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050043129 A1

TITLE: Method for carrying out a multiple gear-shifting in an electronically servo-assisted bicycle gearshift and related gearshift

PUBLICATION-DATE: February 24, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Guderzo, Gianfranco	Arzignano		IT

US-CL-CURRENT: 474/78; 474/70, 474/80

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw. D

☐ 2. Document ID: US 20040254650 A1

L9: Entry 2 of 13 File: PGPB Dec 16, 2004

PGPUB-DOCUMENT-NUMBER: 20040254650
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040254650 A1

TITLE: Multiprocessor control system for cycles, for instance for race bicycles

PUBLICATION-DATE: December 16, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Campagnolo, Valentino	Vicenza		IT
Guderzo, Gianfranco	Tezze-Arzignano (Vicenza)		IT

US-CL-CURRENT: 700/2

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 3. Document ID: US 20040235597 A1

L9: Entry 3 of 13 File: PGPB Nov 25, 2004

PGPUB-DOCUMENT-NUMBER: 20040235597
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040235597 A1

TITLE: Electronically servo-assisted bicycle gearshift and related method

PUBLICATION-DATE: November 25, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Guderzo, Gianfranco	Arzignano		IT

US-CL-CURRENT: 474/70

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 4. Document ID: US 20040225380 A1

L9: Entry 4 of 13 File: PGPB Nov 11, 2004

PGPUB-DOCUMENT-NUMBER: 20040225380
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040225380 A1

TITLE: System and method for controlling the operating functions of a cycle, corresponding units and computer program product

PUBLICATION-DATE: November 11, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Guderzo, Gianfranco	Arzignano		IT

US-CL-CURRENT: 700/19; 700/83, 700/90

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 5. Document ID: US 20040220002 A1

L9: Entry 5 of 13 File: PGPB Nov 4, 2004

PGPUB-DOCUMENT-NUMBER: 20040220002

PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040220002 A1

TITLE: Unit for controlling the operating functions of a cycle

PUBLICATION-DATE: November 4, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Guderzo, Gianfranco	Arzignano		IT

US-CL-CURRENT: 474/80

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 6. Document ID: US 20040093126 A1

L9: Entry 6 of 13 File: PGPB May 13, 2004

PGPUB-DOCUMENT-NUMBER: 20040093126
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040093126 A1

TITLE: System for data transfer, for example for cycles such as competition bicycles

PUBLICATION-DATE: May 13, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Campagnolo, Valentino	Vicenza		IT
Guderzo, Gianfranco	Vicenza		IT

US-CL-CURRENT: 701/1; 280/200

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 7. Document ID: US 7200447 B2

L9: Entry 7 of 13 File: USPT Apr 3, 2007

US-PAT-NO: 7200447
DOCUMENT-IDENTIFIER: US 7200447 B2

TITLE: Multiprocessor control system for cycles, for example for competition bicycles

PRIOR-PUBLICATION:

DOC-ID	DATE
US 20040254650 A1	December 16, 2004

☐ 8. Document ID: US 7184872 B2

L9: Entry 8 of 13 File: USPT Feb 27, 2007

US-PAT-NO: 7184872
DOCUMENT-IDENTIFIER: US 7184872 B2

TITLE: Electronically servo-assisted bicycle gearshift and related method

PRIOR-PUBLICATION:

DOC-ID	DATE
US 20040235597 A1	November 25, 2004

☐ 9. Document ID: US 7042123 B2

L9: Entry 9 of 13 File: USPT May 9, 2006

US-PAT-NO: 7042123
DOCUMENT-IDENTIFIER: US 7042123 B2

TITLE: Bicycle electrical generator hub

PRIOR-PUBLICATION:

DOC-ID	DATE
US 20050285461 A1	December 29, 2005

☐ 10. Document ID: US 7009387 B2

L9: Entry 10 of 13 File: USPT Mar 7, 2006

US-PAT-NO: 7009387
DOCUMENT-IDENTIFIER: US 7009387 B2

TITLE: Tranducer of angular quantities for a cycle

PRIOR-PUBLICATION:

DOC-ID	DATE
US 20030038625 A1	February 27, 2003

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KMC | Draw D

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
bicycl$ and remov$ and guderzo$	13

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Your result set for the last L# is incomplete.

The probable cause is use of unlimited truncation. Revise your search strategy to use limited truncation.

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 11 through 13 of 13 returned.

☐ 11. Document ID: US 6873885 B2

L9: Entry 11 of 13 File: USPT Mar 29, 2005

US-PAT-NO: 6873885
DOCUMENT-IDENTIFIER: US 6873885 B2

TITLE: System for data transfer, for example for cycles such as competition bicycles

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 12. Document ID: US 6757567 B2

L9: Entry 12 of 13 File: USPT Jun 29, 2004

US-PAT-NO: 6757567
DOCUMENT-IDENTIFIER: US 6757567 B2
**** See image for Certificate of Correction ****

TITLE: Multiprocessor control system for cycles, for example for competition bicycles

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 13. Document ID: US 6625523 B2

L9: Entry 13 of 13 File: USPT Sep 23, 2003

US-PAT-NO: 6625523
DOCUMENT-IDENTIFIER: US 6625523 B2

TITLE: System for data transfer, for example for cycles such as competition bicycles

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
bicycl$ and remov$ and guderzo$	13

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First Hit | Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 7009387 B2

L11: Entry 1 of 3 File: USPT Mar 7, 2006

US-PAT-NO: 7009387
DOCUMENT-IDENTIFIER: US 7009387 B2

TITLE: Tranducer of angular quantities for a cycle

PRIOR-PUBLICATION:

DOC-ID	DATE
US 20030038625 A1	February 27, 2003

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 2. Document ID: US 6757567 B2

L11: Entry 2 of 3 File: USPT Jun 29, 2004

US-PAT-NO: 6757567
DOCUMENT-IDENTIFIER: US 6757567 B2
**** See image for Certificate of Correction ****

TITLE: Multiprocessor control system for cycles, for example for competition bicycles

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 3. Document ID: US 6625523 B2

L11: Entry 3 of 3 File: USPT Sep 23, 2003

US-PAT-NO: 6625523
DOCUMENT-IDENTIFIER: US 6625523 B2

TITLE: System for data transfer, for example for cycles such as competition bicycles

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
L9 and @ad<=20030227	3

Display Format: - Change Format

Previous Page Next Page Go to Doc#

L11: Entry 2 of 3 File: USPT Jun 29, 2004

DOCUMENT-IDENTIFIER: US 6757567 B2
** See image for Certificate of Correction **
TITLE: Multiprocessor control system for cycles, for example for competition bicycles

Application Filing Date (1):
20010314

INVENTOR (2):
Guderzo; Gianfranco

Inventor Group (2):
Guderzo; Gianfranco Tezze-Arzignano IT

Brief Summary Text (1):
The present invention relates to control systems for cycles and has been developed with particular attention paid to the possible application to competition bicycles. In any case, the reference to this possible application, and in particular the reference to the application to racing bicycles, must not be interpreted as limiting the possible field of application of the invention.

Brief Summary Text (6):
In brief, the system according to the invention is based on a multiprocessor electronic structure for controlling and managing operation of a cycle, such as a competition bicycle.

Drawing Description Text (3):
FIG. 2 is a schematic illustration of the way in which the various modules making up the system represented in FIG. 1 can be mounted on a cycle, such as a racing bicycle;

Detailed Description Text (1):
The system according to the invention, designated as a whole by 1, is made up of a set of functional blocks interconnected at the level of communication channels. The aforesaid functional blocks may be located in an optimized way on a cycle, such as a racing bicycle, as will be described in greater detail in what follows with reference to FIG. 2.

Detailed Description Text (7):
As may be better seen in the representation of FIG. 2, the block 10 is preferably built as an element that can be selectively removed from the cycle. In this sense, the block 10 may be configured, in particular as regards the communications with the block 20, in such a way as to be at least in part integratable, duplicatable, and emulatable by a further processor block 10a, which may be basically configured as a so-called "user organizer". The latter device is to be deemed in itself known.

Detailed Description Text (28):
The block 10, which is designed to function essentially as a block for managing the system (with functions basically resembling those of a so-called "cycling

computer"), is made, as has been said, in such a way as to be preferably removable from the cycle, with the consequent possibility of detection of insertion or removal both by the block 10 itself and by the block 20, with which the block 10 interacts.

Detailed Description Text (31):
In this connection, it will be appreciated that the block 20 is preferably associated to an element (bracket or the like) which enables removable installation of the block 10. This mode of installation facilitates also communication of the block with the push-buttons 28 and 29, implemented preferably via lines that can be incorporated in the handlebars.

Detailed Description Text (41):
verifying that the system 1 is utilizable, in the sense that all the functional blocks 10, 20 and 30 are present and connected together; for example, removal of the block 10, which has the function of a display unit, is detected in the way just described, i.e., as a result of the removal of the resistor 10R, whereby the block 20 intervenes on the system 1 inhibiting complete functionality of the latter, or, at least, as regards the functions linked to the presence of the block 10;

Detailed Description Text (55):
Again in the context of the communication line 23, the presence of a wire 90 may be noted, which is designed to enable delivery of the supply voltage from the block 30 (connected to which are usually the power-supply sources 50, 50A, 50B) to the block 20. Since the block 10 must be supplied even when it is removed from the system, it has available a power-supply source 10B to its own.

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L11: Entry 2 of 3 File: USPT Jun 29, 2004

US-PAT-NO: 6757567
DOCUMENT-IDENTIFIER: US 6757567 B2
**** See image for Certificate of Correction ****

TITLE: Multiprocessor control system for cycles, for example for competition bicycles

DATE-ISSUED: June 29, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Campagnolo; Valentino	Vicenza			IT
Guderzo; Gianfranco	Tezze-Arzignano			IT

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Campagnolo Srl	Vicenza			IT	03

APPL-NO: 09/805113 [PALM]
DATE FILED: March 14, 2001

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	APPL-DATE
IT	TO2000A0293	March 27, 2000

INT-CL-ISSUED: [07] G05B 19/18

INT-CL-CURRENT:

TYPE IPC	DATE
CIPS G01 C 22/00	20060101
CIPN A63 B 24/00	20060101
CIPS B62 M 25/00	20060101
CIPS B62 M 25/04	20060101
CIPS B62 M 25/08	20060101

US-CL-ISSUED: 700/2; 700/83, 701/1
US-CL-CURRENT: 700/2; 700/83, 701/1

FIELD-OF-CLASSIFICATION-SEARCH: 710/33, 340/432, 280/288.4, 482/901, 482/4, 482/57, 700/2, 700/83, 701/1, 701/48, 307/9.1
See application file for complete search history.

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

Search Selected | Search ALL | Clear

	PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
☐	4780864	October 1988	Houlihan	
☐	5231872	August 1993	Bowler et al.	
☐	5261858	November 1993	Browning	
☐	5298865	March 1994	Denz et al.	324/509
☐	5335540	August 1994	Bowler et al.	
☐	5435315	July 1995	McPhee et al.	
☐	5527239	June 1996	Abbondanza	
☐	5648966	July 1997	Kondo et al.	370/245
☐	6023646	February 2000	Kubacsi et al.	701/1
☐	6049295	April 2000	Sato	340/928
☐	6087938	July 2000	Gitelis et al.	340/567
☐	6148262	November.2000	Fry	701/213
☐	6192300	February 2001	Watarai et al.	701/1
☐	6405340	June 2002	Irvin et al.	714/774
☐	6420797	July 2002	Steele et al.	307/9.1
☐	6430040	August 2002	Masui	361/683
☐	6453262	September 2002	Kitamura	702/145
☐	2003/0207731	November 2003	Oohara	474/70
☐	2003/0216201	November 2003	Takeda	474/70

FOREIGN PATENT DOCUMENTS

FOREIGN-PAT-NO	PUBN-DATE	COUNTRY	CLASS
34 45 617	July 1985	DE	
3 709 587	October 1987	DE	
0 416 325	March 1991	DE	
40 04 981	May 1991	DE	
296 04 853	July 1996	DE	
0 048 662	March 1982	EP	
0 081 807	June 1983	EP	
0 820 923	January 1998	EP	
0 887 251	December 1998	EP	
2 533 025	March 1984	FR	
2 654 698	May 1991	FR	
2 166 598	May 1986	GB	
2 188 459	September 1987	GB	

2 188 489	September 1987	GB
57 014 107	August 1983	JP
5 347 649	December 1993	JP
5 347 650	December 1993	JP
5 347 651	December 1993	JP
WO 89/00401	January 1989	WO
WO 9 214 620	September 1992	WO
WO 93/16891	September 1993	WO

ART-UNIT: 2121

PRIMARY-EXAMINER: Voeltz; Emanuel Todd

ASSISTANT-EXAMINER: Hartman, Jr.; Ronald D

ATTY-AGENT-FIRM: Volpe and Koenig, P.C.

ABSTRACT:

An electronic control system for cycles, for use in association to a set of sensors, a set of actuators, and a set of control members associated to the cycle, comprises: a first processor unit having the function of processing and displaying information; a second processor unit with the function of communication control unit and of interfacing with said set of control members; and a third processor unit having the function of interfacing with said set of sensors and said set of actuators. The first processor unit, the second processor unit, and the third processor unit are connected together via asynchronous bi-directional communication channels.

32 Claims, 7 Drawing figures

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